

Sreenivasa Institute of Technology And Management studies :: Chittoor.

ECE-VLSI System design

M.tech I Sem. CUMMULATIVE ATTENDANCE REPORT UPTO - JAN 2012

| S. No | Name of the Student | VLSI | AICD | DICD | HDL | HSC | ASIC | DICD Lab | Total | % |
|-------------------------|---------------------|------|------|------|-----|-----|------|----------|-------|-------|
| NO.OF CLASSES CONDUCTED | | 30 | 34 | 24 | 36 | 33 | 23 | 24 | 204 | 100.0 |
| 1 | P.SANDHYA | 26 | 31 | 24 | 28 | 28 | 20 | 24 | 181 | 88.7 |
| 2 | K.MAMATHA | 27 | 23 | 19 | 20 | 21 | 19 | 20 | 170 | 83.3 |
| 3 | K.A.VISHNU PRIYA | 28 | 32 | 23 | 29 | 29 | 21 | 24 | 186 | 91.2 |
| 4 | K.HARIKA | 27 | 31 | 19 | 28 | 27 | 19 | 24 | 175 | 85.8 |
| 5 | D.JAHNAVI | 28 | 25 | 19 | 21 | 22 | 18 | 20 | 153 | 75.0 |
| 6 | N.SHARMI PRIYA | 27 | 30 | 19 | 24 | 22 | 20 | 20 | 162 | 79.4 |
| 7 | N.CHANDRA KUMAR | 28 | 21 | 21 | 20 | 22 | 17 | 20 | 170 | 83.3 |
| 8 | K.SUDHAKAR RAO | 12 | 5 | 11 | 10 | 18 | 12 | 4 | 82 | 40.2 |
| 9 | K.N.CHANDRA SEKHAR | 26 | 30 | 20 | 24 | 26 | 20 | 24 | 170 | 83.3 |
| 10 | V.S.CHANDRANADTH | 27 | 28 | 23 | 29 | 28 | 18 | 16 | 169 | 82.8 |
| 11 | P.YUGESH KUMAR | 24 | 29 | 21 | 28 | 26 | 17 | 24 | 169 | 82.8 |
| 12 | I.VIJAY KUMAR REDDY | 28 | 23 | 20 | 24 | 23 | 19 | 20 | 192 | 94.1 |
| 13 | Y.YESWANTH KUMAR | 28 | 27 | 18 | 23 | 25 | 21 | 20 | 185 | 90.7 |
| 14 | P.SUBU RAIDU | 25 | 27 | 20 | 21 | 21 | 21 | 20 | 176 | 86.3 |
| 15 | T.RAVINDRA | 22 | 20 | 17 | 19 | 18 | 19 | 12 | 145 | 71.1 |
| 16 | P.LOKESHWARA | 20 | 22 | 14 | 23 | 20 | 14 | 14 | 127 | 62.3 |

Co-ordinator

HOD/ECE