

**Sreenivasa Institute of Technology And Management studies :: Chittoor.**

ECE-VLSI System design

M.tech I Sem. CUMMULATIVE ATTENDANCE REPORT UPTO - DEC 2011

S. No	Name of the Student	VLSI	AICD	DICD	HDL	HSC	ASIC	DICD Lab	Total	%
NO.OF CLASSES CONDUCTED		10	16	12	16	14	7	12	87	100.0
1	P.SANDHYA	10	16	12	16	14	7	12	87	100.0
2	K.MAMATHA	10	7	9	8	8	6	8	56	64.4
3	K.A.VISHNU PRIYA	10	16	12	16	14	7	12	87	100.0
4	K.HARIKA	10	15	9	16	13	6	12	81	93.1
5	D.JAHNAVI	10	12	9	10	10	6	8	65	74.7
6	N.SHARMI PRIYA	10	14	9	11	10	7	8	69	79.3
7	N.CHANDRA KUMAR	10	11	11	10	10	6	8	66	75.9
8	K.SUDHAKAR RAO	3	0	2	2	7	4	4	22	25.3
9	K.N.CHANDRA SEKHAR	10	14	9	12	12	7	12	76	87.4
10	V.S.CHANDRANADTH	10	14	12	16	14	7	8	81	93.1
11	P.YUGESH KUMAR	10	16	12	16	14	7	12	87	100.0
12	I.VIJAY KUMAR REDDY	10	10	9	11	9	7	8	64	73.6
13	Y.YESWANTH KUMAR	10	10	7	8	10	7	8	60	69.0
14	P.SUBU RAIDU	8	10	9	7	7	6	8	55	63.2
15	T.RAVINDRANATH	8	8	6	7	7	7	8	51	58.6
16	P.LOKESHWARA	5	9	6	16	11	4	12	63	72.4

Co-ordinator

HOD/ECE