

**Sreenivasa Institute of Technology And Management studies :: Chittoor.**

## ECE-VLSI System design

## M.tech II Sem. CUMMULATIVE ATTENDANCE REPORT UPTO -April 2010

S. No	Reg.No.	Name of the Student	FPGA	TT	LPVD	AVDA	SLVDA	CNS	Mixed Signal lab	Total	%
<b>NO.OF CLASSES CONDUCTED</b>			43	37	33	21	26	22	28	210	100
1	09751D5701	C.VISHNU VARDHAN	36	27	27	17	22	19	24	172	81.9
2	09751D5702	J.ARUNA	32	24	24	14	20	14	28	156	<b>74.29</b>
3	09751D5703	K.SAI SRAVANTHI	36	28	27	18	21	18	24	172	81.9
4	09751D5704	N.KAVITHA KUMARI	33	27	22	17	20	13	28	160	76.19
5	09751D5705	M.MAHESH BABU	37	32	29	17	24	20	24	183	87.14
6	09751D5706	M.CHANDRA SEKHAR REDDY	40	36	31	17	25	21	24	194	92.38
7	09751D5707	M.PAVAN KRISHNA	42	33	30	19	26	22	28	200	95.24
8	09751D5708	P.SREEDHAR	35	26	23	14	21	16	28	163	77.62
9	09751D5709	P.KIRAN KUMAR	36	28	27	15	19	19	24	168	80
10	09751D5710	P.SRAVYA	36	26	27	18	21	18	24	170	80.95
11	09751D5711	P.SUKANYA	23	23	20	10	18	13	16	123	<b>58.57</b>
12	09751D5712	S.GOWTHAM	36	28	24	16	22	15	28	169	80.48
13	09751D5713	S.VENKATARAMANA REDDY	35	23	25	17	20	18	20	158	75.24
14	09751D5714	S.SAILA	33	24	22	17	21	16	28	161	76.67
15	09751D5715	G.SEKHARA	34	33	29	16	23	20	28	183	87.14
16	09751D5716	M.SREENIVASULU	38	30	26	19	23	19	28	183	87.14
17	09751D5717	K.SUBRAMANYAM	32	26	23	14	18	16	24	153	<b>72.86</b>
18	09751D5718	V.VISHNU PRIYA	38	27	27	16	22	17	24	171	81.43

Co-ordinator

HOD/ECE